



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Name Application of :

Hung Nguyen

Serial No. :

10/047,515

Group Art Unit :

2111

Filed :

October 26, 2001

Examiner :

Kim, Kenneth S.

For :

A Method To Eliminate Pipeline
Stalls Between Conditional
Branches Using A Mispredict PC
FIFO In A Six-Way Issue
Superscalar DSP

Atty Docket :

/ 01-627

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

Manu Kashyap

September 8, 2005
Date

Signature

SUBMISSION OF FORMAL DRAWINGS PURSUANT TO 37 C.F.R. §1.85

Official Draftsman

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicant hereby substitutes the enclosed formal drawings for those presently in the above referenced application.

LSI Logic Corporation
1621 Barber Lane, MS D-106
Milipitas, CA 95035
408-433-7475

Respectfully submitted,

Timothy Croll

Reg. No. 36,771

Date:

8 Sept 05